<u>Cryptographically</u> <u>Attested</u> <u>Secure</u> <u>Hardware</u>

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My name is Ilia, let's small talk: what are some of your hopes and dreams?

I dream we stop using the word "secure" without context (secure against ___)

<u>Today, privilege implies trust (1/3)</u>



<u>Today, privilege implies trust (2/3)</u>



Today, privilege implies trust (3/3)

orivilege

(shared resources) **Process abstraction is** insufficient.

Leaks via "side channels"

Separate mutually distrusting entities into isolated protection domains



Process boundary

5

Chapter 1: Remotely Attested Execution

Remote Software Attestation (1 / 2)



Trusted HW creates proof for remote user

<u>Remote Software Attestation (2 / 2)</u>



Remote user decides whether or not to trust certificate

Hardware-Assisted Attestation: TPM+TXT



Set associative caches share and leak (1/3)

Accessing address

0x7A9024



Else this is a <u>miss</u>, and causes a <u>fill</u> \rightarrow <u>eviction</u>

Set associative caches share and leak (2/3)



Page tables also leak (2/2)

Microsoft Research, IEEE S&P 2015:

Exploit no-noise side channel due to page faults



Encrypted image compared to public images inside enclave

MIT Sanctum Architecture



<u>Chapter 2: Enclaves via a</u> <u>Security Monitor</u>

Defining properties of an Enclave (1/3)



Create *arbitrary* enclaves A, B Such that their measurements are **equal**



Oh hi! I am <u>authenticated</u>, and you know what to expect from me

Same measurement → same behavior

Defining properties of an Enclave (2/3)



Defining properties of an Enclave (3/3)





Hardware security is hard



Use formal verification to prove equivalence!

<u>Chapter 3: Strong Microarch. Isolation</u> of Protection Domains



"RISC architecture is gonna change everything"

Sharing resources in a simple processor system



Attack Schema (1/2)







Sharing resources in a simple processor system











<u>Isolating in the LLC (6/8)</u>



To isolate enclaves in LLC, allocate exclusively, at region granularity!

Isolating in the LLC (7/8) 12 Address **Physical Page Number** Page Offset translation LLC Line Offset Tag Set Index 15 11 6 12-6=5 "DRAM Region Index" Each region is 1 bits! 4K page in size DRAM Toy example: 3 DRAM region bits **32KB** To isolate enclaves in LLC, allocate Small problem : A DMA buffer

exclusively, at region granularity!

Isolating in the LLC (8/8)



<u>Hardware-assisted</u> <u>Isolation</u>

Maintain an invariant: <u>TLB entries are safe</u>!

HW enforces invariants <u>at page walks</u>

SW updates invariants and causes <u>TLB shootdowns</u>







Isolating enclave page tables

Should this VA use enclave's tables?

OS could spy on enclave's page table entries

fix by...

Implement <u>enclave-private</u> <u>page tables</u>



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Remote attestation of enclaves (1/5)



Remote attestation of enclaves (2/5)



Remote attestation of enclaves (3/5)



Remote attestation of enclaves (4/5)



Remote attestation of enclaves (5/5)



The first party <u>sends</u> (via the encrypted channel) **private code/data** to the trusted enclave.

The enclave's **initial state** and **isolation** are <u>authenticated</u> (and <u>trusted</u>).

The enclaved application **must not have** leaks or vulnerabilities; The enclave <u>performs its computation</u> (which may communicate with the OS or other parties, use other data, send results to the first party, etc.).

Untrusted OS, hypervisor, etc.

The SM guarantees it remains isolated.

